

miriac® MPX1011 (EOL)

System on Module based on NXP® QorlQ® P1011 CPU

- up to 1 GB soldered DDR2 memory
- up to for SerDes up to 3.125 GHz multiplexed across controllers
- two 208 Pin Zero Force Connectors, that make all I/O and bus signals available to the carrier board
- High performance, low power, single core CPU
- The P1011 processor provides single core solutions for the 533 MHz to 800 MHz performance range



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The miriac[®] MPX1011 CPU Module is the second of a series of QorlQ[®] based SoMs by MicroSys and pin compatible to the MPX2020 products. The devices in these two platforms are software compatible, sharing the e500 Power Architecture core and peripherals, as well as being fully software compatible with the existing PowerQUICC processors. This enables you to create a product with multiple performance points from a single board design.

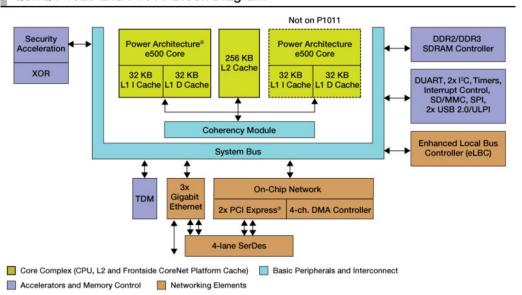






Block Diagrams

QorlQ P1020 and P1011 Block Diagram



Features ·

CPU	
Architecture	PowerPC
Processor	NXP® QorlQ® P1013 CPU, single 500v2 core @ 600 - 1055 MHz, 256 kB L2 Cache with ECC, also configurable as SRAM and stashing memory
Memory	
Flash	up to 512 MB NAND Flash
Flash Card	1x SD/MMC
High Speed IO	
SerDes lanes	up to five SerDes to 3.125 GHz multiplexed across controllers, e.g. • 2x PCI Express® interfaces • 2x SATA interfaces
USB 2.0	2x USB 2.0
Mechanical	
Formfactor	MPX-1, 77 mm x 66 mm
Software / Additional	

Lin

Microware OS-9

Software Support

- VxWorksQNX
- others are avialable on request

Our standard product versions offer what we consider to be the optimum configuration in terms of performance, price, usage and TDP. The product features lists specify the maximum range of functions per interface. However, not all interfaces or functions are always available in parallel. Flexible SERDES multiplexing is one of the reasons for this. In addition, we provide multiple memory expansion options and are also happy to accommodate specific customer wishes. So do not hesitate to contact us directly to discuss your desired configuration.



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