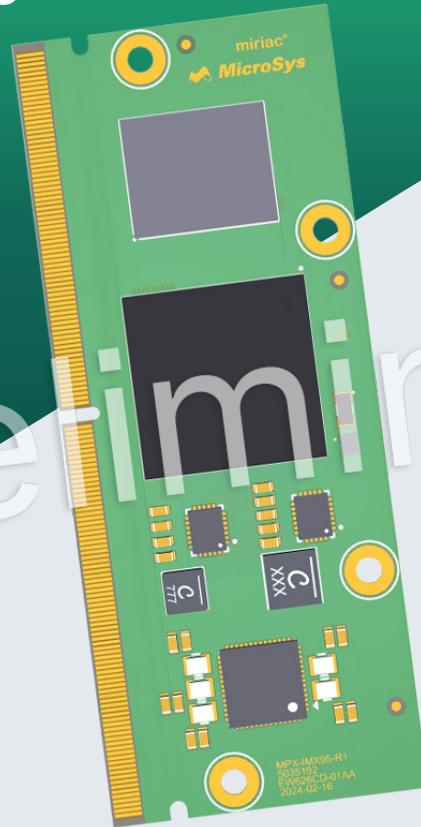


SoMs Arm® Architecture

miriac® MPX-i.MX95

Arm® Cortex®-A55



Preliminary

COMING
SOON

Highlights

- 6 Arm® Cortex®-A55 cores at up to 1,8 GHz, 1 Arm® Cortex®-M7
- 1 Arm® Cortex®-M33
- 1 Arm® Mali™ 3D GPU 310





Product Description

The MPX-iMX95 is based on multiple heterogeneous processing units including up to 6 cores 1,8 GHz Arm® Cortex®-A55 plus two independent real-time co-processors: a 800MHz Arm® Cortex®-M7 and an Arm® Cortex®-M33 up to 333MHz. It offers high-speed data processing alongside high-performance real-time and safety/low-power uses.



Features

CPU	
Architecture:	Arm® Cortex®-A55
Processor:	NXP® i.MX95 CPU: 6 Arm® Cortex®-A55 cores at up to 1,8 GHz 1 Arm® Cortex®-M7 @ 800MHz 1 Arm® Cortex®-M33 @ 333MHz
DRAM:	16 GByte LPDDR5 (x32) up to 6.4GT/s (w/ Inline ECC)
Memory	
SD Card:	2x (up to) interface on connector (boot source)
Octal-Flash:	1x Interface on connector (boot source)
eMMC:	1x up to 64GB (Onboard boot source)
EEPROM:	Yes
Graphic	
Graphic:	3D GPU, OpenGL® ES 3.2, Vulkan® 1.2, OpenCL™ 3.0, 2D GPU
Ethernet	
10GbE:	1x XFI/USXGMII
1GbE:	2x RGMII
High Speed IO	
USB 3.0:	1x
USB 2.0:	1x
PCIe:	2x 1-lane
Security / Safety	
Tamper:	2x
Operating Condition	
Additional:	Dev Kit available for immediate start, includes power supply, cables. Linux on SD card
Operating Condition	
Operational Temperature:	-40/+85°C
Temperatursensor:	Yes
Supply Voltage:	5V
RTC:	RV-3028-C7
Mechanical	
Dimensions:	82x35mm

Connector Type:

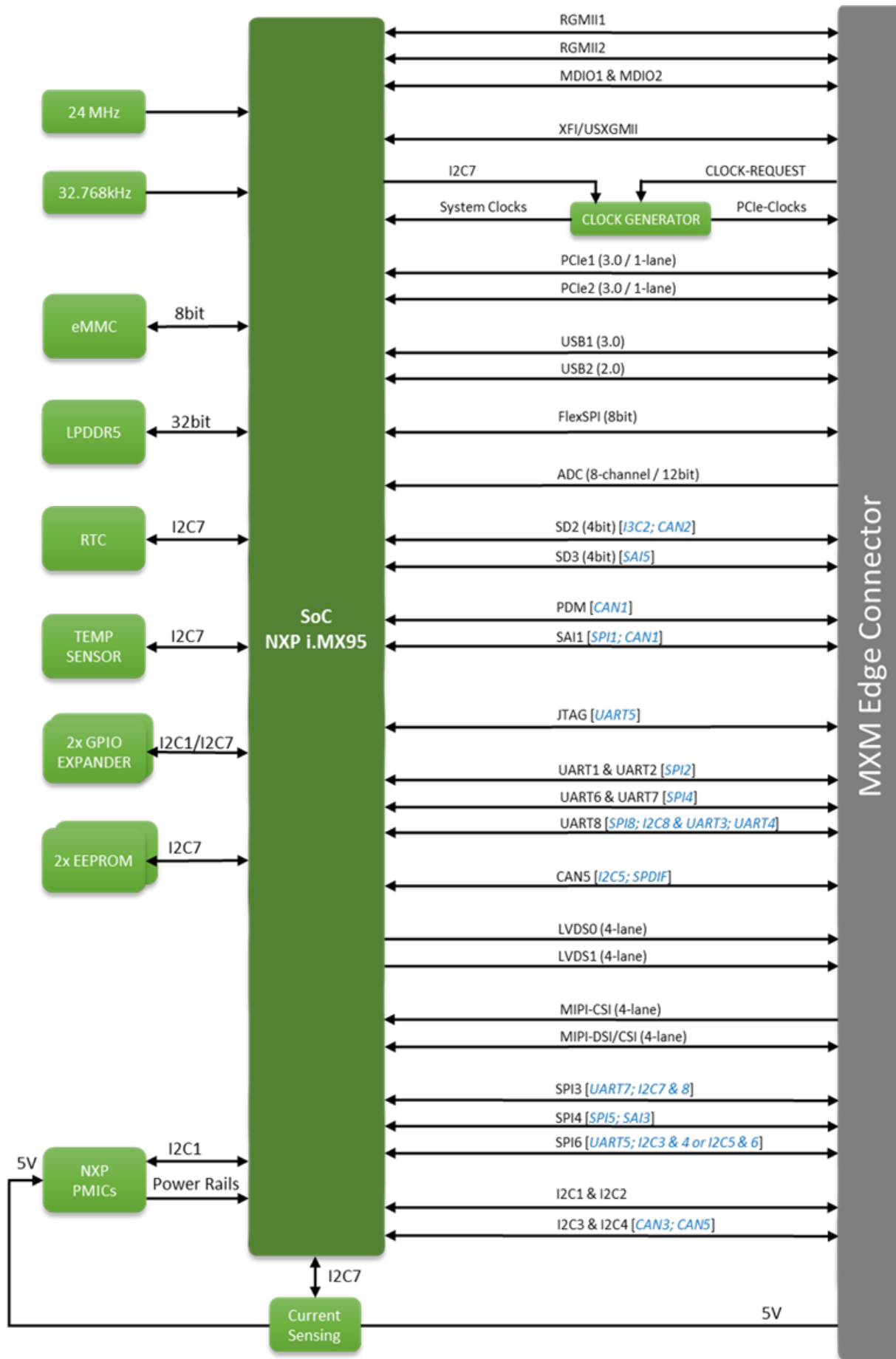
MXM3.0

General Note:

Our standard product versions offer what we consider to be the optimum configuration in terms of performance, price, usage and TDP. The product features lists specify the maximum range of functions per interface. However, not all interfaces or functions are always available in parallel. Flexible SERDES multiplexing is one of the reasons for this. In addition, we provide multiple memory expansion options and are also happy to accommodate specific customer wishes. So do not hesitate to [contact us](#) directly to discuss your desired configuration.



Block Diagrams




Legend: [InterfacesXY] are typical multiplexing options

V1.0 / 7.11.2024



Related Products

Name	Description	Image
CRX-i.MX95		<p>Preliminary</p> 



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