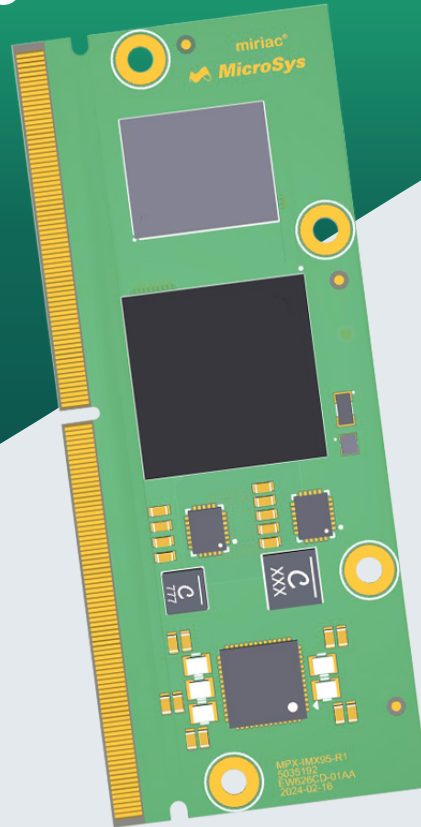SoMs Arm® Architecture

miriac® MPX-i.MX95

Arm Cortex®-A55

SINGLE PAIR ETHERNET

COMING SOON

Highlights

- 6 Arm® Cortex®-A55 cores at up to 1,8 GHz, 1 Arm® Cortex®-M7

- 1 Arm® Cortex®-M33

- Single Pair Ethernet

NXP Gold Partner

Product Description

The MPX-iMX95 is based on multiple heterogeneous processing units including up to 6 cores 2.0GHz Arm Cortex®-A55 plus two independent real-time co-processors: a 800MHz Arm Cortex-M7 and an 250MHz Arm Cortex-M33. It offers high-speed data processing alongside high-performance real-time and safety/low-power uses.

Features

CPU	
Architecture:	Arm® Cortex®-A55
Processor:	NXP® i.MX95 CPU: 6 Arm® Cortex®-A55 cores at up to 1,8 GHz, 1 Arm® Cortex®-M7, 1 Arm® Cortex®-M33
DRAM:	Up to 6.4GT/s x32 LPDDR5 (w/ Inline ECC)
Memory	
Boot Flash:	Up to 64GB eMMC
Ethernet	
RGMII:	2x GbE TSN
Security / Safety	
Connector Type:	MXM3.0
Software Support:	- Linux
	- Others (on request)
	- All I/O pins available on 314-pin edge connector
Operating Condition	
Power Supply Voltage:	Single DC power input (+9 V to +30 V)
Mechanical	
Dimensions:	82 mm x 50 mm
Additional:	- Dev Kit available for immediate start, includes power supply, cables. Linux on SD card

General Note:

Our standard product versions offer what we consider to be the optimum configuration in terms of performance, price, usage and TDP. The product features lists specify the maximum range of functions per interface. However, not all interfaces or functions are always available in parallel. Flexible SERDES multiplexing is one of the reasons for this. In addition, we provide multiple memory expansion options and are also